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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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EXAMINER

YUN, EUGENE

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2682

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Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 09/601,122	Applicant(s) MURDOCH, GRAHAM	
	Examiner Eugene Yun	Art Unit 2682	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1 and 37-72 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1 and 37-72 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 23 February 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____. | 6) <input type="checkbox"/> Other: ____. |

DETAILED ACTION

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1, 37-40, 44-51, 54, 55, 57-59 are rejected under 35 U.S.C. 102(b) as being anticipated by Urbas et al. (US 5,481,262 "IDS").

Referring to Claim 1, Urbas teaches a transceiver including:

An antenna 4 and 11 (fig. 2) for receiving a first signal and transmitting a second signal;

Signal processor means 7-10 (fig. 2) for receiving from the antenna a third signal indicative of the first signal; and

Modulator means 6 (fig. 2) disposed between the antenna and the signal processor means for providing a fourth signal to the antenna for forming the second signal, the modulator means varying the impedance between the antenna and the signal processor means for providing the antenna with a dual Q factor, the Q factor being high for the first signal and low for the second signal (see col. 7, lines 15-24).

Referring to Claim 37, Urbas teaches a transceiver including:

An antenna 4 and 11 (fig. 2) for receiving a first radio frequency (RF) electromagnetic signal and transmitting a second RF electromagnetic signal;

Signal processor means 7-10 (fig. 2) for receiving from the antenna a third electrical signal based on the first RF electromagnetic signal; and

Modulator means 6 (fig. 2) disposed in series between the antenna and the signal processor means for providing a fourth electrical signal to the antenna to produce the second RF electromagnetic signal, the modulator means varying the series impedance between the antenna and the signal processor means (see col. 7, lines 15-24).

Claim 49 has similar limitations to Claim 37.

Referring to Claim 50, Urbas teaches a passive transponder including:

An antenna 4 and 11 (fig. 2) for receiving and transmitting a first radio frequency (RF) electromagnetic signal and a second RF electromagnetic signal respectively;

Signal processor means for: receiving a third electrical signal from the antenna which is derived from the first RF electromagnetic signal; and providing a fourth electrical signal derived from the third electrical signal (see 7-10 in fig. 2);

Power storage means in parallel with the signal processor means for absorbing some of the power of the third electrical signal, the absorbed power being subsequently used by the transponder (see TRANSPONDER POWER and PROGRAMMING VOLTAGE in fig. 2);

Modulator means 6 (fig. 2) disposed in series between the antenna and the power storage means for selectively varying the impedance therebetween to generate the second RF electromagnetic signal (see col. 7, lines 15-24); and

A mixer 10 (fig. 2) for producing a fifth signal by combining the fourth electrical signal with a sub-carrier, the fifth signal being provided to the modulator means.

Referring to Claim 54, Urbas teaches an antenna 4 and 11 (fig. 2) for receiving and transmitting a first radiofrequency (RF) electromagnetic signal and a second RF electromagnetic signal respectively, the antenna including:

A tuned coil in which the first signal generates a first current and which supports a second current for generating said second signal (see COIL ASSY of 4 and 11 of fig. 2); and

Modulator means 6 (fig. 2) disposed in series with the antenna, said first and second currents flowing through said modulator for providing said coil with a simultaneous dual Q factor, the Q factor being high for the first current and low for the second current (see col. 7, lines 15-24).

Referring to Claim 57, Urbas teaches a transceiver including:

An antenna 4 and 11 (fig. 2) for receiving a first radio frequency (RF) electromagnetic signal and transmitting a second RF electromagnetic signal;

Signal processor means 7-10 (fig. 2) for receiving from the antenna a third electrical signal indicative of the first signal;

Modulator means 6 (fig. 2) disposed in series between the antenna and the signal processor means for providing a fourth electrical signal to the antenna to produce

Art Unit: 2682

the second signal, the modulator means varying the voltage across the antenna in a substantially stepwise manner to effect a variation in the current flowing through the antenna between a low and a high value for allowing transmission of the second signal without substantially affecting the receiving efficiency of the antenna (see col. 7, lines 15-24).

Claim 59 has similar limitations as Claim 57.

Referring to Claim 38, Urbas also teaches the transceiver as a transponder and the first and second signals modulated at a first frequency and second frequency respectively, the first and second frequencies being different from each other (see col. 7, lines 15-24).

Referring to Claim 39, Urbas also teaches a passive transponder and the signal processor means includes processing circuitry and power storage means, wherein some of the power provided by the third signal is stored in the power storage means for subsequently powering the transponder (see TRANSPONDER POWER and PROGRAMMING VOLTAGE in fig. 2).

Referring to Claim 40, Urbas also teaches the impedance varied between the high and low value at a rate greater than the DC slew rate for the third signal (see col. 7, lines 15-24).

Referring to Claim 44, Urbas also teaches the voltage across the antenna modulated or varied in a predetermined manner to generate the second signal 6 (fig. 2).

Referring to Claim 45, Urbas also teaches the modulation or variation in antenna voltage corresponding to a proportional variation in the antenna current (see col. 5, lines 60-66).

Referring to Claim 46, Urbas also teaches the modulator means varying a low impedance which is disposed in series between the antenna and the signal processor means to cause a variation in the voltage across the antenna (see fig. 2).

Referring to Claim 47, Urbas also teaches the low impedance less than 10% of the total load impedance seen by the antenna (see col. 6, lines 2-11).

Referring to Claim 48, Urbas also teaches the impedance modulated with an RF sub-carrier and data modulated onto the sub-carrier for transmission (see col. 5, lines 60-66).

Referring to Claim 51, Urbas also teaches the modulator means varying the impedance in accordance with the fifth signal (see col. 7, lines 15-24).

Referring to Claim 55, Urbas also teaches the first current or a signal derived from the first current provided to a signal processing means 7-10 (fig. 2) whereby the modulator means varies the impedance between the coil and the processing means (see col. 7, lines 15-24).

Referring to Claim 58, Urbas also teaches the first signal including a carrier signal and the variation of the current between the low and the high value occurring within less than or about one period of the carrier signal (see col. 5, lines 15-22).

Art Unit: 2682

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 41-43, 52, 53, 56, and 60-72 are rejected under 35 U.S.C. 103(a) as being unpatentable over Urbas in view of Schuermann (US 5,374,930).

Referring to Claim 60, Urbas teaches a transceiver including:

An antenna 4 and 11 (fig. 2) for receiving a first radio frequency (RF) electromagnetic signal having a first predetermined frequency and, in response thereto, generating a second electrical signal;

Receiving circuitry being responsive to the second signal (see 7-10 of fig. 2); and

A modulator 6 (fig. 2) disposed in series between the antenna and the tuning circuitry for varying the impedance there between such that the second signal generates a third electrical signal in the antenna at a second predetermined frequency and the antenna transmits a fourth RF electromagnetic signal derived from the third signal (see col. 7, lines 15-24).

Urbas does not teach tuning circuitry for providing the antenna with a resonant frequency at or about the first predetermined frequency. Schuermann teaches tuning circuitry for providing the antenna with a resonant frequency at or about the first predetermined frequency (see 56 and 58 of fig. 1). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to provide

the teachings of Schuermann to said device of Urbas in order to increase data transfer rates while maintaining cost and size of the circuitry.

Referring to Claim 70, Urbas teaches a tuned antenna including:

A coil (see COIL ASSY of 4 and 11 of fig. 2) for receiving a first radio frequency (RF) electromagnetic signal having a first predetermined frequency and thereby generating a second electrical signal; and

A modulator 6 (fig. 2) disposed for providing a varying impedance such that the second signal generates a third electrical signal in the coil at a second predetermined frequency whereby the coil transmits a fourth RF electromagnetic signal derived from the third signal (see col. 7, lines 15-24).

Urbas does not teach a capacitor connected in parallel with the coil for providing the antenna with a resonant frequency at or about the first predetermined frequency.

Schuermann teaches a capacitor connected in parallel with the coil for providing the antenna with a resonant frequency at or about the first predetermined frequency and the modulator disposed in series with the capacitor (see 56 and 58 of fig. 1). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to provide the teachings of Schuermann to said device of Urbas in order to increase data transfer rates while maintaining cost and size of the circuitry.

Claim 71 has similar limitations as Claim 60.

Claim 72 has similar limitations as Claim 70.

Referring to Claims 41, 52 and 56, Urbas does not teach the impedance as a resistance. Schuermann teaches the impedance as a resistance which is switched

between a predetermined value and a negligible resistance 58 (fig. 1). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to provide the teachings of Schuermann to said device of Urbas in order to increase data transfer rates while maintaining cost and size of the circuitry.

Referring to Claims 42, 62 and 63, Schuermann also teaches the antenna as a coil 36 (fig. 1) tuned by a capacitor connected in parallel with the coil 56 (fig. 1).

Referring to Claim 43, Urbas also teaches the modulator means varying the impedance between the antenna and the signal processor means, such that the antenna simultaneously has a high Q factor for signals received by the antenna and a low Q factor for signals transmitted from the antenna (see col. 7, lines 15-24).

Referring to Claim 53, Schuermann also teaches the power storage means including a capacitor 46 (fig. 1).

Referring to Claim 61, Urbas also teaches the first and second predetermined frequencies substantially different (see col. 7, lines 15-24).

Referring to Claim 64, Schuermann also teaches the modulator connected in series with the capacitor (see fig. 1).

Referring to Claim 65, Urbas also teaches the receiving circuitry, in response to the second signal, actuating the modulator to provide the third signal (see signal path to 6 of fig. 2).

Referring to Claim 66, Urbas also teaches the third signal modulated in accordance with a data signal specific to that transceiver (see 6 in fig. 2).

Referring to Claim 67, Urbas also teaches the data signal stored in the receiving circuitry 5 (fig. 2) and selectively provided to the modulator 6 (fig. 2).

Referring to Claim 68, Urbas also teaches the second signal as the current generated in the antenna by the first signal (see signal path from 4 to 5 to 6 in fig. 2).

Referring to Claim 69, Urbas also teaches the second signal as the voltage induced across the tuning circuitry by the first signal (see signal path from TRANSPONDER POWER to 5 to 6 in fig. 2).

Response to Arguments

6. Applicant's arguments filed 5/6/2005 have been fully considered but they are not persuasive.

First, the applicant argues that the Urbas reference does not teach a single antenna for receiving a first signal and transmitting a second signal. The examiner refers to fig. 2 of the Urbas reference to show that the two antennas 4 and 11 are both within a **common coil assembly**, therefore making one single antenna which can receive a first signal and transmit a second signal. Therefore, the Urbas reference does indeed teach a single antenna for receiving a first signal and transmitting a second signal. In addition, a single antenna which receives a first signal and transmits a second signal is a very common feature that is well known in the art.

Second, the applicant argues that the Urbas reference does not teach a modulator disposed in series with an antenna. It is shown in fig. 2 that there is a modulator connected in series to an antenna.

Art Unit: 2682

Finally, the applicant argues that the Urbas reference does not teach a Q factor being high for a first signal and low for the second signal. Referring back to col. 7, lines 15-23, an output of "1" can mean the same as the Q factor being high and an output of "0" meaning a low Q factor. Therefore, it can be shown in the above passage and fig. 3A that an initial output of a first signal as "1" can go through any combination or series of flip-flops and eventually be output from a flip flop with a low Q factor or "0" output before transmission.

In addition, it is inherent to one skilled in the art that there must be a change in impedance in a RFID circuit between receiving and transmitting signals, meaning that there would automatically be a change in Q-factor or quality factor in the antenna. If there was no change, there would be a reflection of the signal in the RFID circuit. Therefore, it should be inherent in most RFID circuits that there is a change in Q-factor between when a signal is received and when a signal is transmitted.

Conclusion

7. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any


Art Unit: 2682

extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Eugene Yun whose telephone number is (571) 272-7860. The examiner can normally be reached on 9:00am-6:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nick Corsaro can be reached on (571) 272-7876. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


Eugene Yun
Examiner
Art Unit 2682

EY


NICK CORSARO
PRIMARY EXAMINER